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E Waingold, M Taylor, D Srikrishna, V Sarkar, W ... - Computer, 1997 - ieeexplore.ieee.org
 ... specification is used to automatically **generate** a behavioral ... benchmarks down to
 configuration binary **code** and ran ... In the **near** term, Raw architectures will ...
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A Agarwal, S Amarasinghe, R Barua, M Frank, W Lee, ... - Proceedings of the Second SUIF Compiler
 Workshop, 1997 - 129.215.96.3
 ... from whole program analysis for resource **allocation** 9 ... our initial compiler can **generate**
 unoptimized **code** ... extensible, very fast dynamic **code** generation system. ...
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DSP Traditional - IEEE MICRO, 1732 - doi.ieeeecs.org
 ... We exploit this feature to **generate** software-pipelined ... New register **allocation**
 techniques use multiple banked ... as well as statistical **code** information (ranges ...
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P Paulin, F Karim, P Bromley - Proceedings of the conference on Design, automation and test ..., 2001
 - doi.ieeecomputersociety.org
 ... of source **code** lines, the tool will **generate** a comprehensive ... development process,
 where the target is **near** 100% C ... The **code** generated requires 24.5 MIPS of the ...
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[... heterogeneous multicore architectures for versatile media processing via customized long instruction ... - all 3 versions »](#)

TF Chen, CM Hsu, SR Wu - Circuits and Systems for Video Technology, IEEE Transactions ..., 2005 -
 ieeexplore.ieee.org
 ... algorithms that are needed to **generate** the suitable ... The new ISA for CLIW and **code**
 generation are ... allow hardware flexibly enough to **allocate** processing resources ...
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C Ussery, O Levia, J Gostomski, G Derti, MA ... - 2001 - freepatentsonline.com
 ... 264 and compiles the **code** to **generate** a binary ... The compiler 268 generates VLIW **code**
 that manages ... addition, the compiler 268 can **allocate** instruction slots to ...
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C Liem, F Breant, A Wu - 2002 - freepatentsonline.com
 ... In another embodiment, the **allocation** of the plurality of data to the ... In one embodiment,
 the method includes **generating** machine executable **code** for the ...
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ZS Chamski, M Durantoni, A Cohen, C Eisenbeis, P ... - Ambient intelligence: impact on embedded
 system design, 2003 - Springer
 ... will require more work to **generate** low-level **code** (**code** compaction, **code** partitioning
 for different ... dependences (based on a cyclic **allocation** of resources ...

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[Software development for high-performance, reconfigurable, embedded multimedia systems - all 3 versions »](#)

A La Rosa, L Lavagno, C Passerone - Design & Test of Computers, IEEE, 2005 - [ieeexplore.ieee.org](#)
... architecture, hardware and software compiler tools **generate** the binary **code** and the bit ... figures, to optimize **instruction** scheduling and register **allocation**. ...

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[A reconfigurable **processor** architecture and software development environment for embedded systems - all 2 versions »](#)

F Campi, A Cappelli, R Guerrieri, A Lodi, M Toma, ... - Parallel and Distributed Processing Symposium, 2003. 2003 - [ieeexplore.ieee.org](#)
... are defined, one to be used to **generate code** for the ... In this manner register **allocation** in the function that ... of clock cycles spent in each line of source **code**. ...

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HiPAR-DSP: a parallel VLIW RISC processor for real time imageprocessing applications

JP Wittenburg, M Ohmacht, J Kneip, W Hinrichs, P ... - Algorithms and Architectures for Parallel Processing, 1997. ..., 1997 - [ieeexplore.ieee.org](#)

... Multimedia with its image coding **tasks** as well as ... In this case, this **compiler** generated code is equivalent ... and even/odd register numbers are **allocated** by the ...

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The multiframe trace scheduling **compiler** - all 13 versions »

PG Lowney, SM Freudenberger, TJ Karzes, WD ... - The Journal of Supercomputing, 1993 - Springer ... The **task** of the **instruction** scheduler (IS) within this ... with shifts and bitwise logicals;

long integers by ... mode, which is supported by the **compiler**, allows each ...

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EVALUATION OF SOME SUPERSCALAR AND VLIW PROCESSOR DESIGNS - all 4 versions »

JG HOLM - 1992 - [crhc.uiuc.edu](#)

... **task**-level parallelism ... **compiler** that consists of classical optimizations 3. In addition to ... ing tools to schedule code for very **long instruction word** VLIW and ...

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[book] A Self-reconfiguring Computer System - all 5 versions »

ZMG Saleeba, Monash University Dept. of Computer ... - 1998 - [csse.monash.edu.au](#)

... 6.3 A **long** buering stream ... Figure 1.1: **Instruction** complexity and rate ... both in the types of **task** which they perform well and in the size of **tasks** which can ...

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North-Holland Microprocessing and Microprogramming 18 119861 251-262 251 - all 2 versions »

P Marwedel - Microprocessing and Microprogramming - Is12-[www.informatik.uni-dortmund.de](#)

... Another **task** of MSSR is to assign memory locations ... Traditional **compiler** techniques like [4] are optimal only for ... Let MI(si) be the **instruction** allocated to si. ...

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Cost-aware design-time/run-time memory management methods and apparatus - all 2 versions »

P Marchal, JI Gomez, D Bruni, F Catthoor - 2006 - [freepatentsonline.com](#)

... based solutions as proposed earlier in the **compiler** and system ... and thus which data needs to be **allocated**, the algorithm ... times of all **tasks** within the **task**-set. ...

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[ps] Kawa—compiling dynamic languages to the Java VM - all 6 versions »

P Bothner - Proceedings of the USENIX 1998 Technical Conference, FREENIX ... - [usenix.org](#)

... I will discuss the **compiler** later, but rst we will ... The **task** then is to map each Scheme type into a ... algorithms that work on unsigned pre-**allocated** bignums; they ...

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Customizing Embedded Processors for Specific Applications - all 4 versions »

A Kumar, M Balakrishnan, MK Jain, A Gangwar - [cse.iitd.ernet.in](#)

... superscalar in embedded systems because the **task** of identifying ... either after scheduling like a typical **compiler**[14], or ... is advanced in order to **allocate** a new ...

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[ps] [Kawa: Compiling Scheme to Java - all 5 versions »](#)

P Bothner - Lisp Users Conference, 1998 - lfcia.org

... I will discuss the **compiler** later, but rst we will ... The **task** then is to map each Scheme type into a ... algorithms that work on unsigned pre-allocated bignums; they ...

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[Models of Machines and Modules for Mapping to Minimise Makespan in Multicomputers - all 3 versions »](#)

MG Norman, P Thanisch - informatik.uni-erlangen.de

... If this minimum makespan is too **long**, the programmer ... level of modularity for finding such **tasks** might be ... level of modularity such as the **compiler's** abstract ...

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